


Personal Data

Recent Photo	
Name (Arabic)	ايمان سالم عبد العزيز سالم
Name (English)	Eman Salem Abdelaziz Salem
Department	Electrical Engineering Department.
College	Benha Faculty of Engineering
Academic Degree	B.Sc. of Engineering and Technology. M.Sc. of Engineering and Technology. Ph.D. in Engineering Sciences.
Academic Position	Lecturer at Benha Faculty of Engineering since 2020. Instructor at Benha Faculty of Engineering since 2010.
Mobile 1	+2 01000424059
Email (university)	eman.salem@bhit.bu.edu.eg
Email (alternative)	comm.eng.emy@Gmail.com
Web Sites	http://bu.edu.eg/staff/emansalem5-publications https://www.scopus.com/authid/detail.uri?authorId=57204921893 https://orcid.org/0000-0003-1490-3908 https://scholar.google.com.eg/citations?hl=en&user=JMwm9u0AAAAJ https://independent.academia.edu/emansalem18 https://www.linkedin.com/in/eng-emy-835b20195/ https://www.researchgate.net/profile/Eman Salem Abdel Aziz2

Education

University	Degree obtained	Year
Benha Faculty of Engineering <i>General specialty: Electronic and Communication Systems.</i>	B.Sc. of Engineering and Technology	2009
	M.Sc. of Engineering and Technology.	2014
	Ph.D. of Engineering. Ph.D Thesis Title "ASIC Design and Implementation of Physical Layer for High Speed Ethernet System.	2020

Language skills (Excellent, Very Good, Good, Basic)

Language	Reading	Speaking	Writing
Arabic	Excellent	Excellent	Excellent
English	Very Good	Good	Very Good

Skills and Expertise

Programming Languages	C++, C++ Object Oriented OOP, MATLAB, VHDL Language. Python Language Microsoft Office
Software Tools	Experience in ASIC Digital Design EDA Tools: Synopsys Design Compiler Cadence SOC Encounter Cadence Virtuoso Tool. Model SIM. Xilinx Ise MATLAB (m file- Simulink) Code::Blocks

Hardware Tools	FPGA and ASIC Chips
Academic Courses	Data Structure and Algorithm Micro Electronic Circuits Design of Electronic Circuits MATLAB Computer Programming "C++, C++ OOP" Python Language " Computer Fundamentals and Programming" Cryptography and Cryptanalysis

Scientific Publications	<ol style="list-style-type: none"> <li data-bbox="493 709 1474 865">1. E.Salem , A.Zekry, H.Labeb,, and RM.Tawfik, " FPGA implementation of 1000base-x Ethernet physical layer core ", International Journal of Engineering & Technology, 7 (4) (2018) 2106-2112. <li data-bbox="493 919 1474 1075">2. E. Salem, A.Zekry, and RM. Tawfeek," ASIC Design and Implementation of 25 Gigabit Ethernet Transceiver with RS_FEC", International Journal of Electrical and Electronic Engineering & Telecommunications Vol. 9, No. 3, May 2020.
--------------------------------	--

Supervised Projects	<ol style="list-style-type: none"> <li data-bbox="500 1243 1487 1312">1. BFE Students Graduation Project "Computer Network Infrastructure with Security" 2021
----------------------------	--